



Koneru Lakshmaiah Education Foundation

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"Design Techniques for High-Speed Analog Communication Circuits"

10 February 2021

In Association with IETE Vijayawada Chapter

Venue: Online through Webex



Electronics &
Communication
Engineering



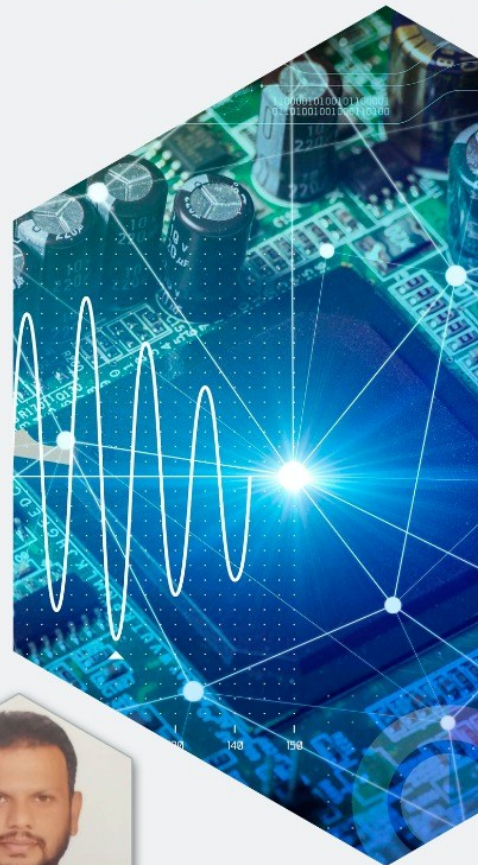
NATIONAL SEMINAR ON DESIGN TECHNIQUES FOR HIGHSPEED ANALOG COMMUNICATION CIRCUITS

10th February 2021 @ 10 AM

IN ASSOCIATION WITH



IETE
VIJAYAWADA
CHAPTER



Resource Person

Shameer Basha Yerragudi

Senior Analog Design Engineer
Analog Port



Organized by

MERG Group, Department of ECE, KL

REGISTRATION LINK: [HTTPS://FORMS.GLE/X5RSGDZFDME5QIM98](https://forms.gle/X5RSGDZFDME5QIM98)

Department Chair
Dr. V. K. Mittal
Professor, ECE

HODs
Dr. M. Suman
Professor and Head, ECE, KLVZA
Dr. M. Goutham
Professor and Head, ECE, KLJ

MERG Group Head
Dr. K. Srinivasa Rao
Professor, ECE

Program Convener
Dr. G. V. Ganesh
Assoc. Professor, ECE
ganeshgvp@kluniversity.in
+91 9908303099

Program Co Conveners
Mr. Syed Shameem
Assoc. Professor, ECE
K. Rajesh Babu
Assoc. Professor, ECE

Objective

The main objective of a guest lecture on Design Techniques for High-Speed Analog Communication Circuits would likely be to introduce you to the specific challenges and considerations involved in creating circuits that can handle fast-moving analog signals. Some of the key areas such a lecture might cover: High-frequency limitations of standard analog components, Signal integrity concepts, High-speed amplifier design, Data converters, PCB layout techniques.

Overall, the guest lecture should equip you with a foundational understanding of the complexities involved in designing analog circuits that can function effectively in high-speed communication applications.

Description

This lecture will delve into the exciting world of designing circuits that handle high-speed analog signals used in communication systems. The session also covers High-performance amplifiers (linearity, gain, bandwidth), Data converters (ADCs, DACs) for interfacing between analog and digital domains, Mixers for frequency conversion, Active filters for signal conditioning, Importance of high-speed analog circuits in modern communication systems, Future trends in high-speed circuit design

The lecture will likely include circuit diagrams, simulations, and practical design examples to illustrate the concepts. You might also have the opportunity to discuss real-world design challenges and trade-offs faced by engineers in this field.

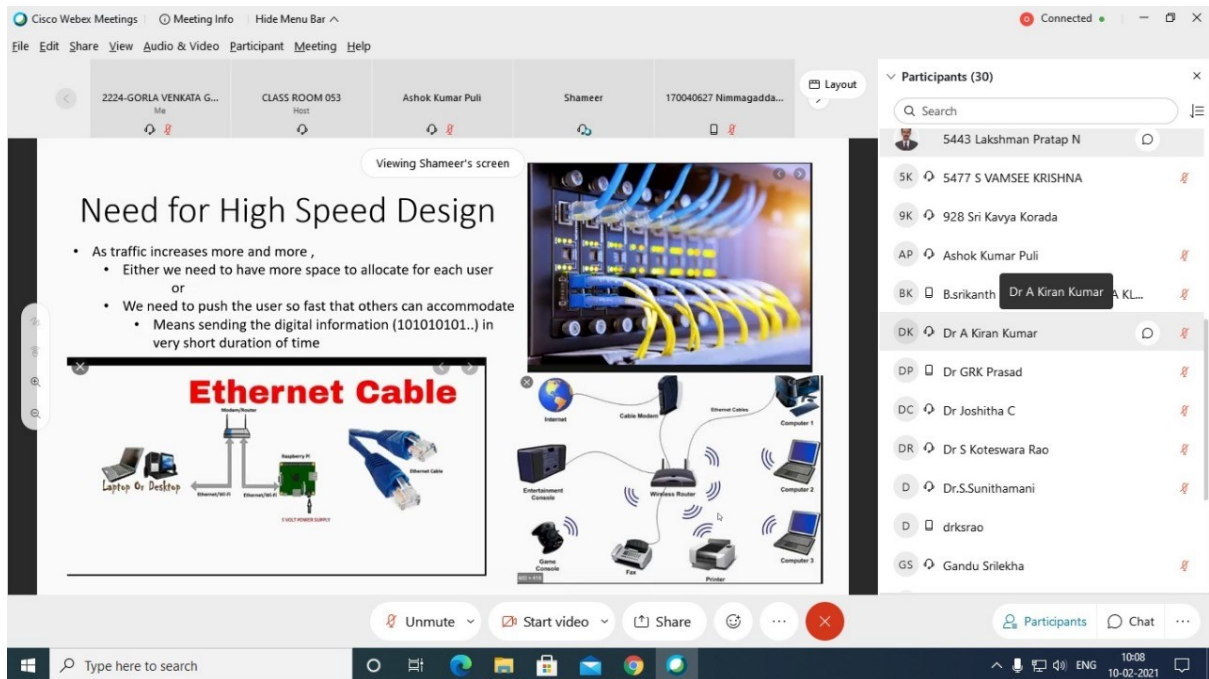
Outcome

The key outcomes of the guest lectures includes:

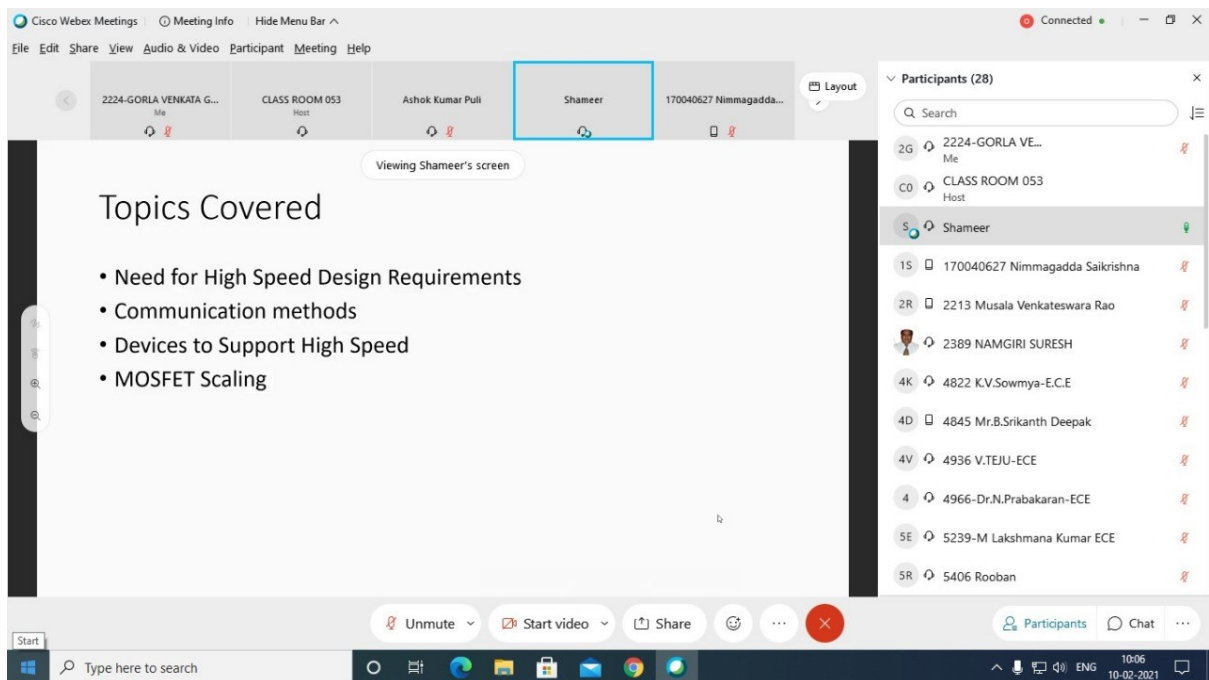
- ✓ Stronger foundation in high-speed communication principles
- ✓ Enhanced knowledge of relevant circuit building blocks
- ✓ Practical design techniques for signal integrity
- ✓ Exposure to advanced design concepts
- ✓ Broadened appreciation for real-world applications

Overall, this guest lecture will provide valuable knowledge and insights for anyone interested in the exciting field of high-speed analog communication circuit design. It will equip you with the understanding to approach the design of such circuits with a more informed and practical perspective.

Photos



Screenshot of the online Meeting



Screenshot of the online Meeting

National online Seminar on "Design Techniques for High-Speed Analog Communication Circuits" by Micro Electronics Research Group, Department of ECE, KLEF – Reg.

Registrar <registrar@kluniversity.in>

Tue 2/9/2021 10:39 AM

To: PRESIDENT <president@kluniversity.in>; Havish <havish@kluniversity.in>; Raja H Koneru <krh@kluniversity.in>; Dr. S S Mantha <ssmantha@kluniversity.in>; Dr. Venkat <drvenkat@kluniversity.in>; Vice Chancellor - KLU <vc@kluniversity.in>; N Venkat Ram <venkatram@kluniversity.in>; Dr. Subrahmanyam <smkodukula@kluniversity.in>; Dr Jagadeesh Anne <drjagadeesh@kluniversity.in>; kamineni jayanendra babu <kjbabu_ipe@kluniversity.in>; Deputy Registrar <deputyregistrar@kluniversity.in>; Vijay Babu Sunkara <vijayababusunkara@kluniversity.in>; A Krishna Rao <akr@kluniversity.in>; Assistant Registrar-Office of Registrar <asstregistrar@kluniversity.in>; Sreenivasulu <nikhi_bt@kluniversity.in>; Dr. B. Jayakumar Singh <jksingh@kluniversity.in>; Dr. Burra V L S Prasad <pburra@kluniversity.in>; Vemuri Praveen Kumar <vemuripraveen@kluniversity.in>; sudhamani <sudhamani@kluniversity.in>; Mahi <drmahi_bt@kluniversity.in>

📎 1 attachments (526 KB)

MERG, ECE, Seminar poster.jpg;

Ref: KLEF/RO/HOD-ECE/2020-21

Date: 09-02-2021

Orders of the Vice-Chancellor dt.09-02-2021

CIRCULAR

Sub: National online Seminar on "**Design Techniques for High-Speed Analog Communication Circuits**" by Micro Electronics Research Group, Department of ECE, KLEF – Reg.

Ref: Letter dated 08.02.2021 from Dr.K. Srinivasa Rao, Professor, ECE and Head-MERG forwarded by Dr.M. Suman, HoD-ECE.

This is to inform that Micro Electronics Research Group, Department of ECE, KLEF, in association with IETE, Vijayawada chapter, is conducting a National online seminar on "**Design Techniques for High-Speed Analog Communication Circuits**" for all the students, faculty and research scholars as per the details given below.

Speaker : **Mr. Shameer Basha Yerragudi**,
Senior Analog design engineer, Analog Port,
Date and Time : **10.02.2021 (Wednesday) at 10.00 a.m.**

Program Convener : Dr. G.V.Ganesh, Associate Professor, Department of ECE, Mobile No.9908010199, E-mail Id: ganeshgorla_ece@kluniversity.in

Program Co Conveners: Mr. Syed Shameem & Mr. K. Rajesh Babu

Department Chair : Dr. V. K. Mittal, Professor, ECE

Program Chair : Dr.M. Suman, Professor & HOD, ECE
Dr.M. Gowtham, Professor & HoD-ECE, KLH

MERG Group-Head : Dr.K. Srinivasa Rao, Professor, ECE

Link for registration: <https://forms.gle/x5RSgDZFdME5Q1m98>

All the interested students, faculty members and research scholars can participate in the seminar.

Poster of the seminar is attached herewith.

REGISTRAR

Encl: Poster

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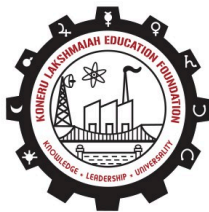
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Mail to: All students

Mail to: All faculty

Mail to: All research scholars

Thanks & Regards



Prof. Y. V. S. S. V. Prasada Rao
Ph.D(Mech. Engg.), DPM., MBA (Fin & HR), FICWA
REGISTRAR

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Participants List

University ID	Student Name	Course	SIGNATURE
190040007	AKELLA P.B. SRI KRISHNA SASTRI	ECE	P.B. Krishna Sastri
190040012	ALAPANA KRISHNA VAMSI REDDY	ECE	A.K. Vamsi Reddy
190040042	BANDI NANDITHA	ECE	B. Nanditha
190040049	BATTULA RAVI TEJA	ECE	B. Ravi Teja
190040123	DOMMETI SAI RAMYA	ECE	D. Sai Ramya
190040147	GOLLAPUDI V N S S R KOUNDINYA	ECE	G. V. N. S. S. R. Koundinya
190040152	GORRE SAHITHI	ECE	G. Sahithi
190040172	GURRALA JAHNAVI	ECE	G. Jahnavi
190040194	KALAPALA THANUSHA	ECE	K. Thanusha
190040243	KOMMANABOYINA JYOTHI KIRAN BABU	ECE	K. J. Kiran Babu
190040262	KOTA SIVA SWATHI	ECE	K. Swathi
190040305	MANDADAPU PUJITHA	ECE	M. Pujitha
190040362	NARANDAS VIJAYA LAKSHMI	ECE	N. Vijayalakshmi
190040378	P NITHIN	ECE	P. Nithin
190040389	PALLAPOLU KALYANCHAKRAVARTHY	ECE	P. Kalayachakravarthy
190040394	PAMURU VISHAL REDDY	ECE	P. Vishal Reddy
190040472	SHAIK MASTAN VALI	ECE	S. M. Vali
190040504	SWARNA RAMA VAMSI	ECE	S. Rama Vamsi
190040506	SYAMALA NAGA KOTI REDDY	ECE	S. Naga Koti Reddy
190040510	SYKAM NIKHITHA	ECE	S. Nikhitha
190040535	VADLA SREEDHAR	ECE	V. Sreedhar
190040571	VINUKONDA SRUTHI HASINI	ECE	V. Sruthi Hasini
190040575	VYSHNAVI VUTUKURI	ECE	V. Vyshnavi
190040579	YALAMARTHI JAYARAM PRADEEP	ECE	Y. Jayaram Pradeep
190040587	YEMINENI CHAKRAVARTHI	ECE	Y. Chakravarthy
190040591	YERUVA NITESH KUMAR REDDY	ECE	Y. Nitesh Kumar Reddy
190040624	DODDI TARAKA RAVI RAJ	ECE	D. Taraka Ravi Raj
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190040660	BANDI PRANAYA KAVYA	ECE	B. Pranaya Kavya
190040675	ROHIT RATHOD	ECE	R. Rohit Rathod
190049034	JAMMULA SURENDRA BABU	ECE	J. Surendra Babu
2000040038	GANAPAVARAPU JYOTHI SWAROOP	ECE	G. Jyothi Swaroop
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2000040110	PALNATI HARI PRAKASH	ECE	P. Hari Prakash
2000040136	DUTTA SIVAMANI	ECE	D. Sivamani
2000040210	TIRUMALASETTY DISNEY	ECE	T. D. Disney
2000040214	BOLLOJU VEERA BHADRA SRINISH	ECE	B. Veera Bhadra Srinish
2000040279	SIFA CLEMENCE	ECE	S. Sifa Clemence
2000040284	BALAM RUFUS	ECE	B. Rufus
2000040292	RAHUL SUBHASH BHANGE	ECE	R. Subhash Bhanghe
2000040038	GANAPAVARAPU JYOTHI SWAROOP	ECE	G. Jyothi Swaroop
2000040001	ANUMOLU PADMAVATHI	ECE	A. Padmavathi

2000040018	B L GNAPIKA	ECE	R.L. Gnapika
2000040031	DADANI POOJA	ECE	D. Pooja
2000040066	KAMBHAMPATI HAVEELA	ECE	K. Haveela
2000040073	JOHN KAMAL KATEKELA	ECE	John Kamal K
2000040080	KORUKONDABATTAR VARUN KRISHNA	ECE	K. Varunkrish
2000040106	PATNAIK MOHAN SANDEEP	ECE	P.M. Sandeep
2000040114	PONNURU SRI VARSHA	ECE	P. Sri Varsha
2000040130	SHAIK ALTHAF HUSSAIN	ECE	S.A. Hussain
2000040143	TAMMA MONIKA REDDY	ECE	T. Monikalady
2000040145	KOLLURU TARAKA CHANDRA SRIKAR	ECE	Chandra
2000040148	TUMMALA HRUDAY	ECE	T. Hruday
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2000040201	MUTYALA HORA SATYA MADHU BABU	ECE	M. Madhu
2000040206	SHAIK MUNNA	ECE	S. Munna
2000040263	RANAGANI YOJANA SNOHI	ECE	R. SNOHI
2000040275	MADDIPATLA LALITHA SRUJANA	ECE	M. Lalitha Srujana


Program Convener :  Dr. G. V. Ganesh, Associate Professor, ECE

Associate Professor
Department of ECE
KLEF
Green Fields, Vaddeswaram
Guntur Dist., A.P. PIN-522 502

HoD

:  Dr. M. Suman, Professor, ECE

Dr. M. SUMAN
Professor & Head
Department of ECE
KLEF
Green Fields, Vaddeswaram,
Guntur Dist., A.P. PIN- 522 502


Associate Professor
Department of ECE
KLEF
Green Fields, Vaddeswaram,
Guntur Dist., A.P. PIN-522 502